

**REPLY UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2189**

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Application No.:	10/621,072	§	Examiner:	Flournoy, Horace L.
Filed:	July 16, 2003	§	Group/Art Unit:	2189
Inventors:		§	Atty. Dkt. No:	6000-34900
Mark S. Moir		§		P8428
Victor M. Luchangco		§		
Maurice Herlihy		§		
		§		
Title:	Software Transactional	§		
	Memory for Dynamically	§		
	Sizable Shared Data	§		
	Structures	§		
		§		
		§		
		§		

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**RESPONSE TO FINAL ACTION OF DECEMBER 14, 2006**

**Mail Stop AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Final Action of December 14, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.